

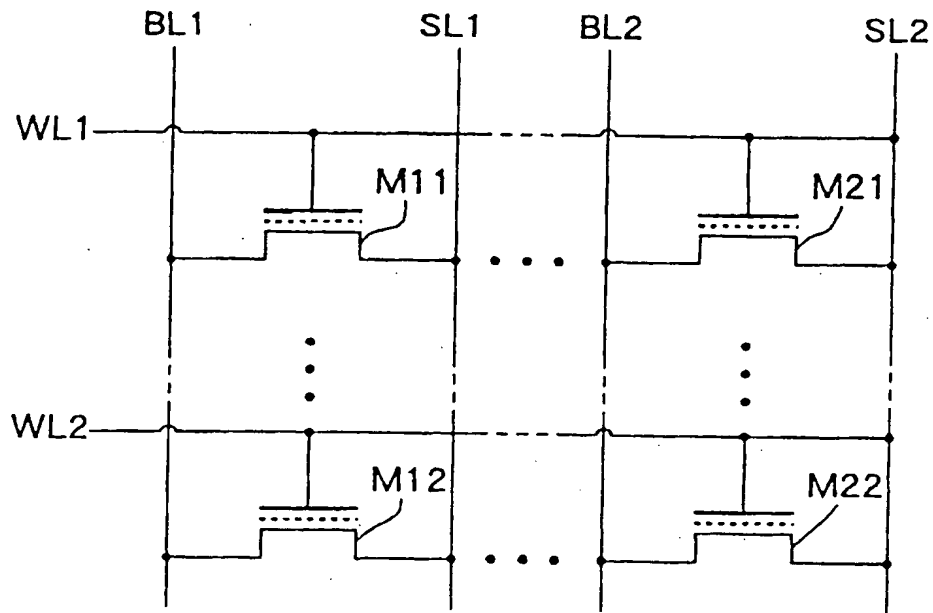
METHOD OF ERASING NON-VOLATILE SEMICONDUCTOR MEMORY
DEVICE AND SUCH NON-VOLATILE SEMICONDUCTOR MEMORY DEVICE

Inventor: Ichiro Fujiwara
Docket No. 075834.00465

Robert J. Depke, Holland & Knight LLC - (312) 263-3600

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FIG.1



100

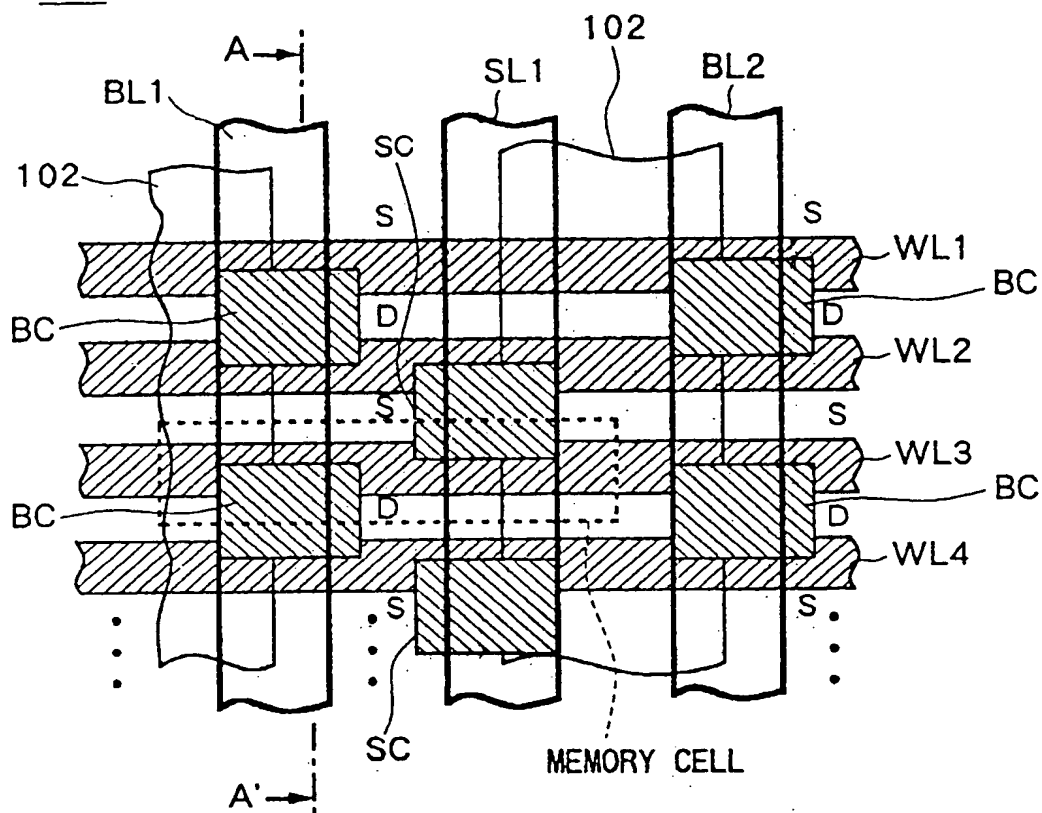


FIG.3

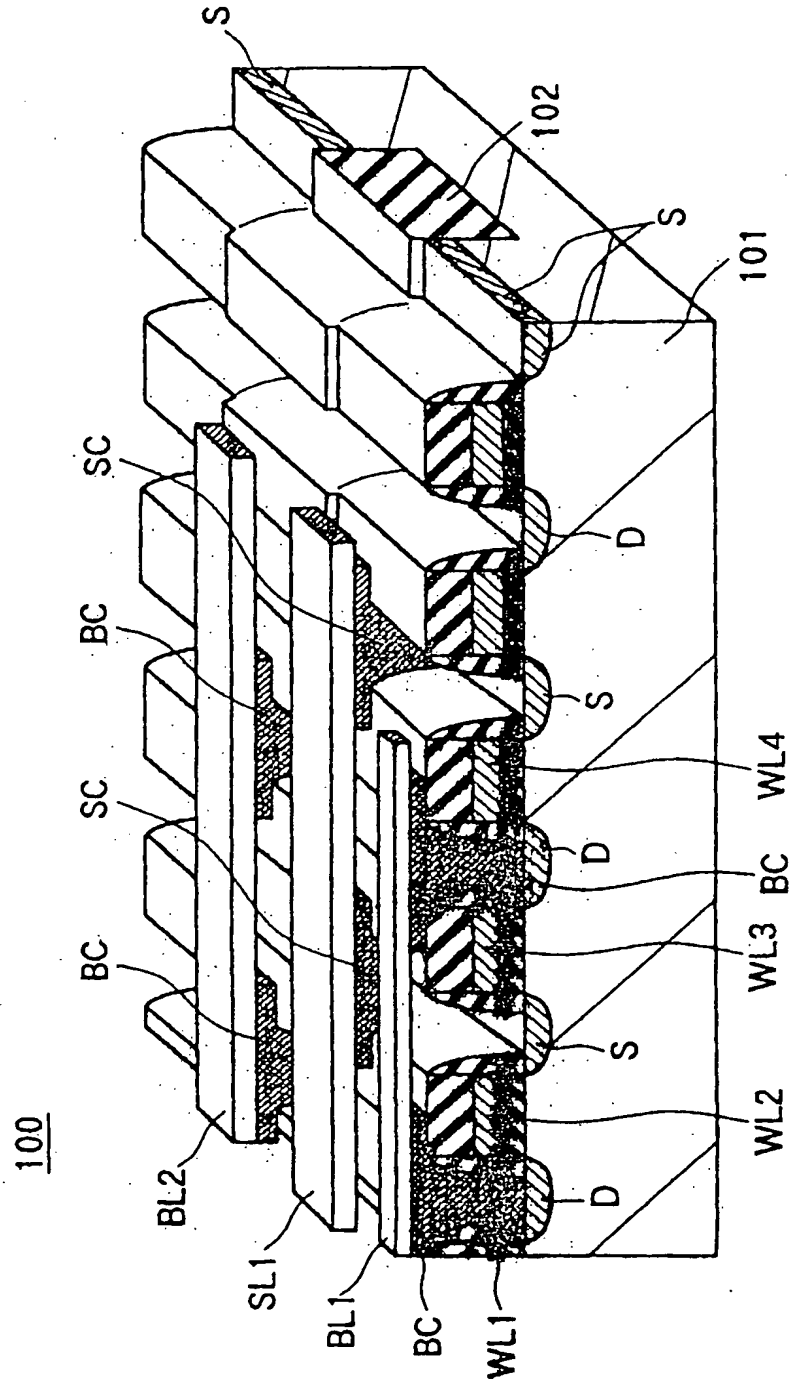
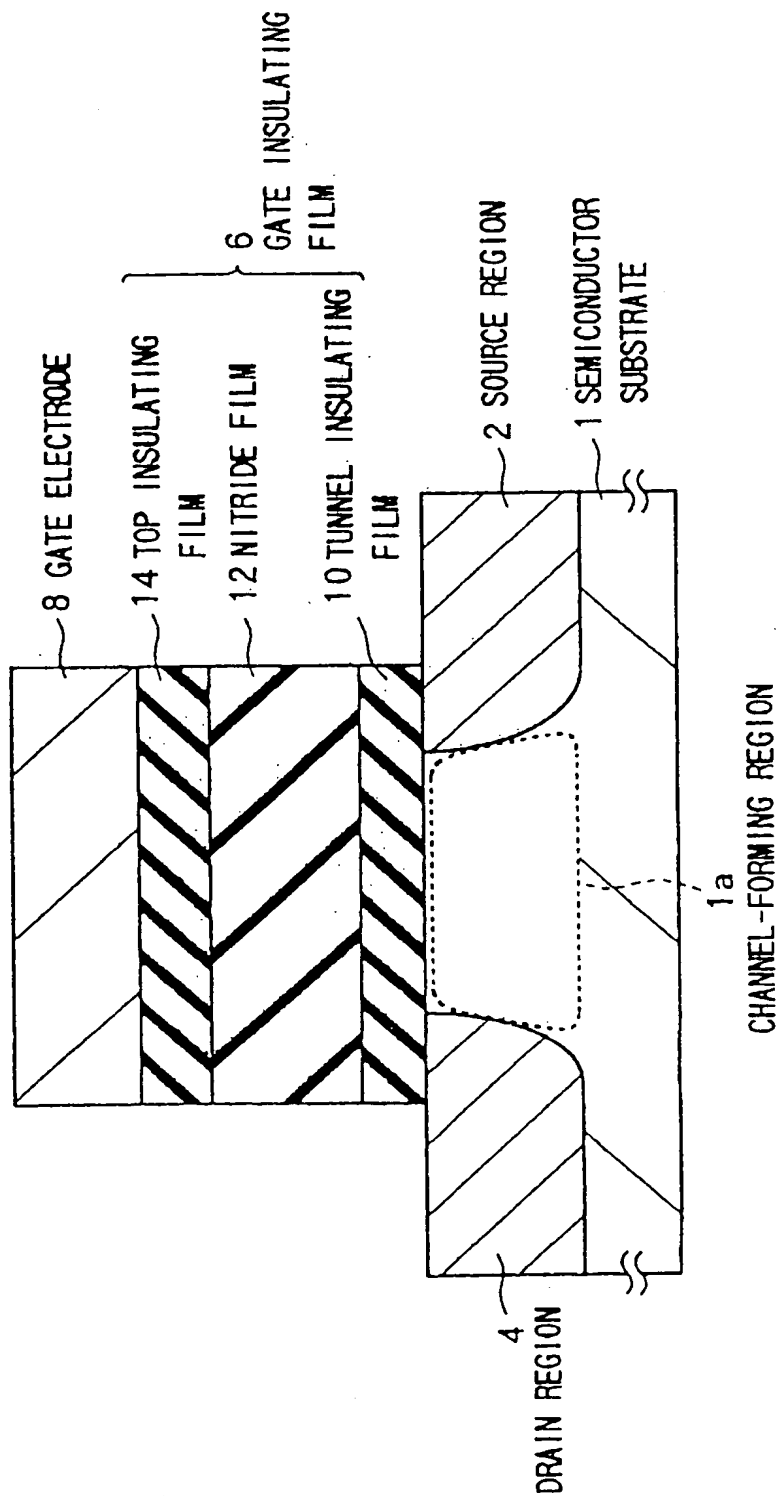


FIG.4

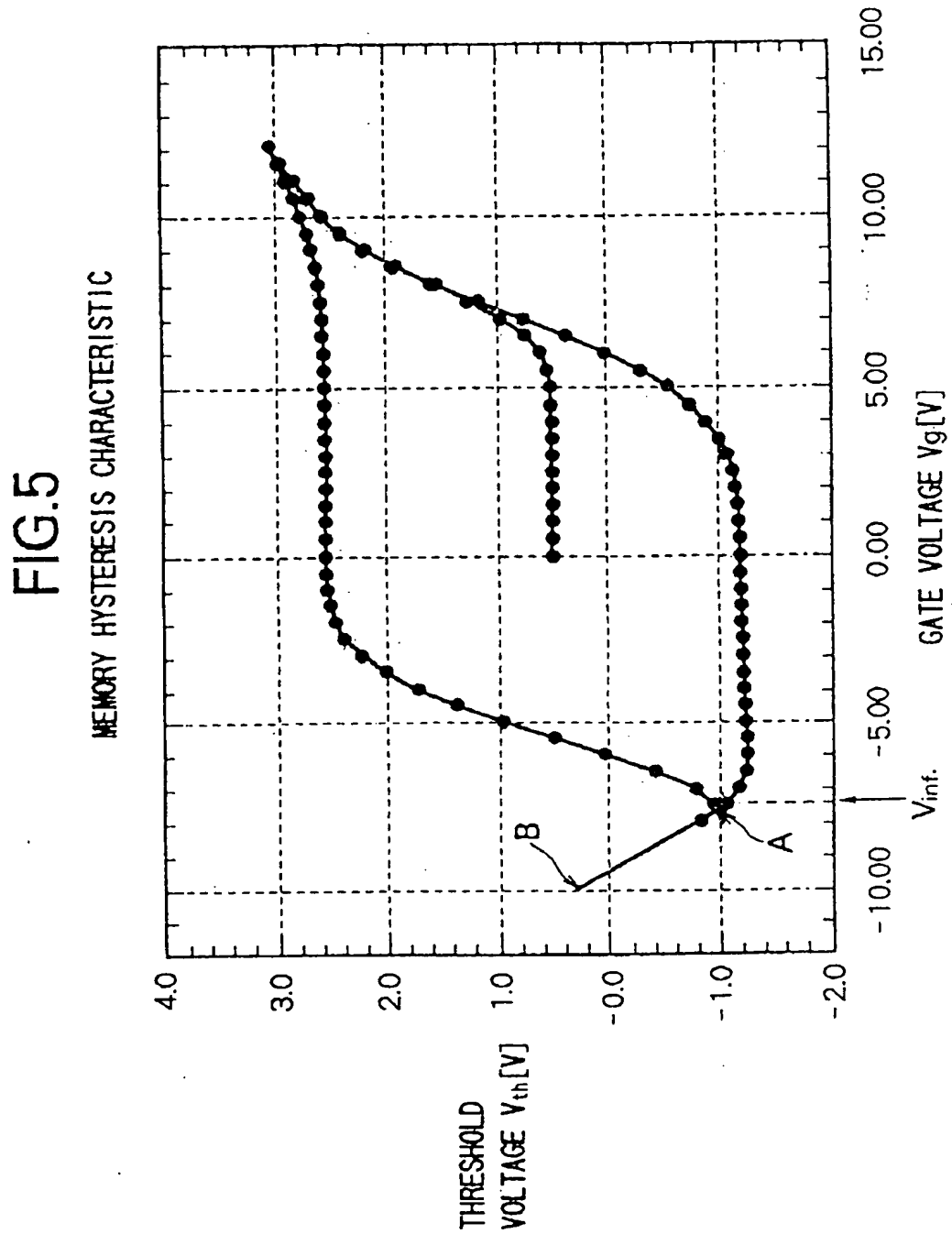


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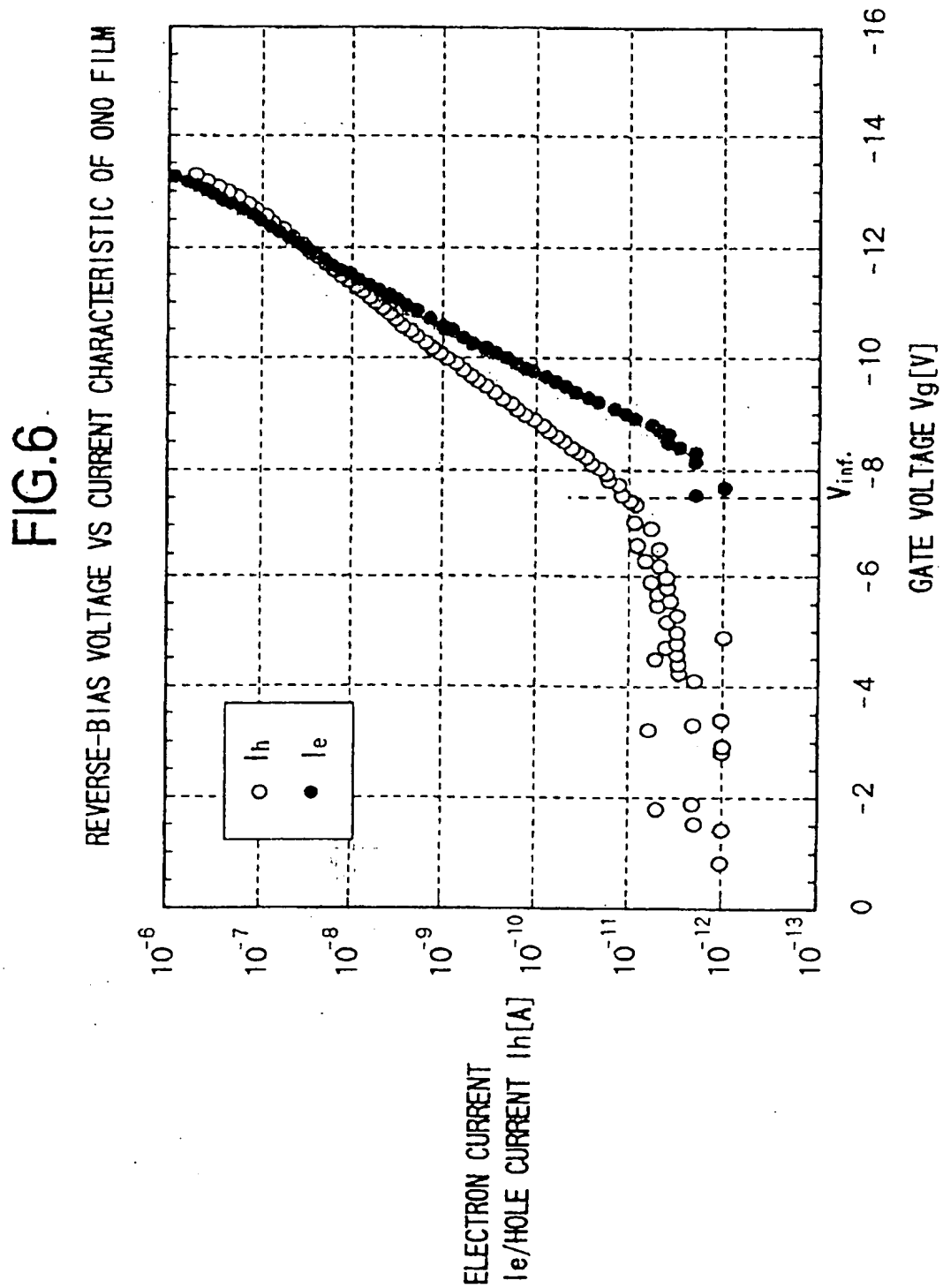


FIG. 7

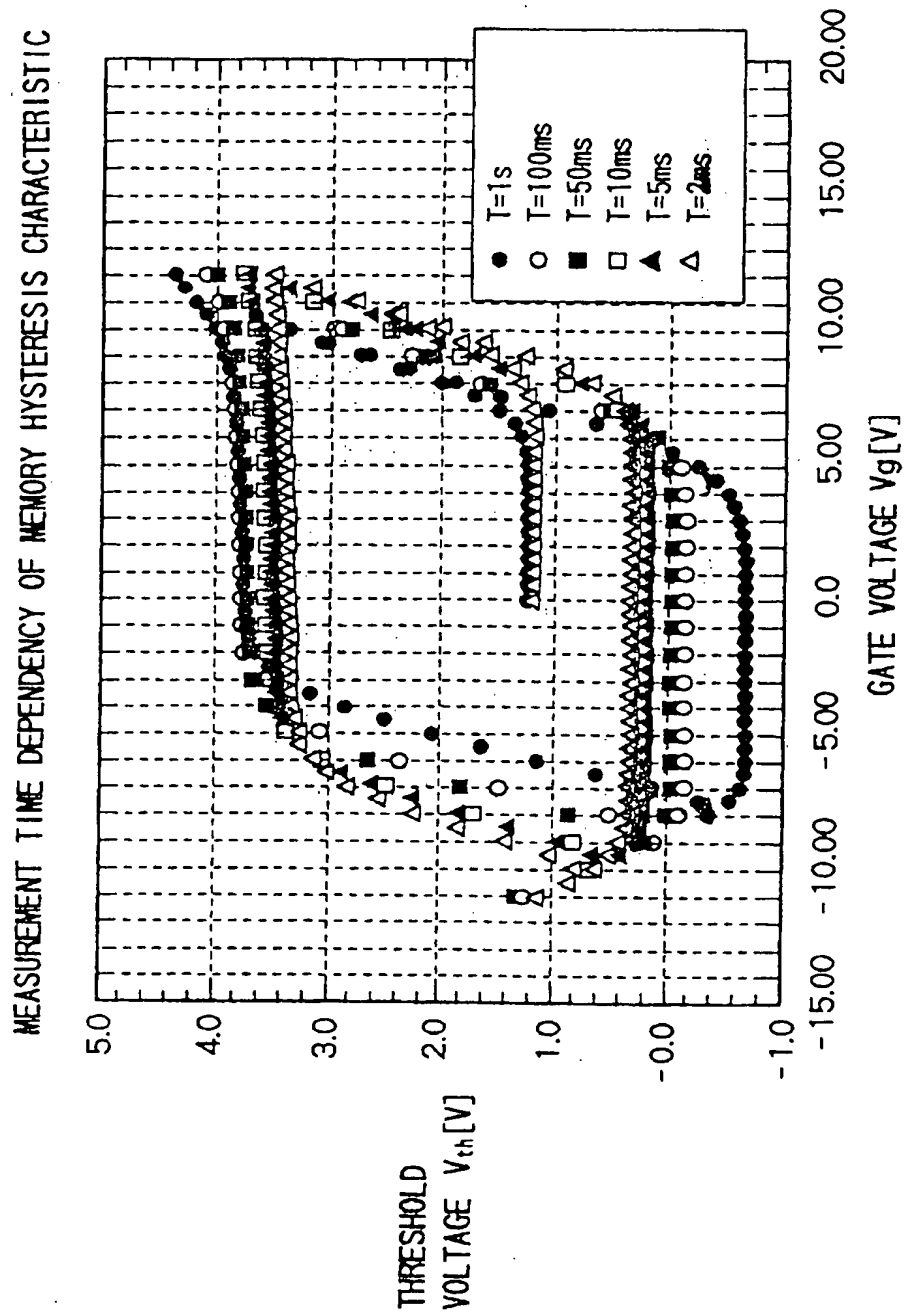
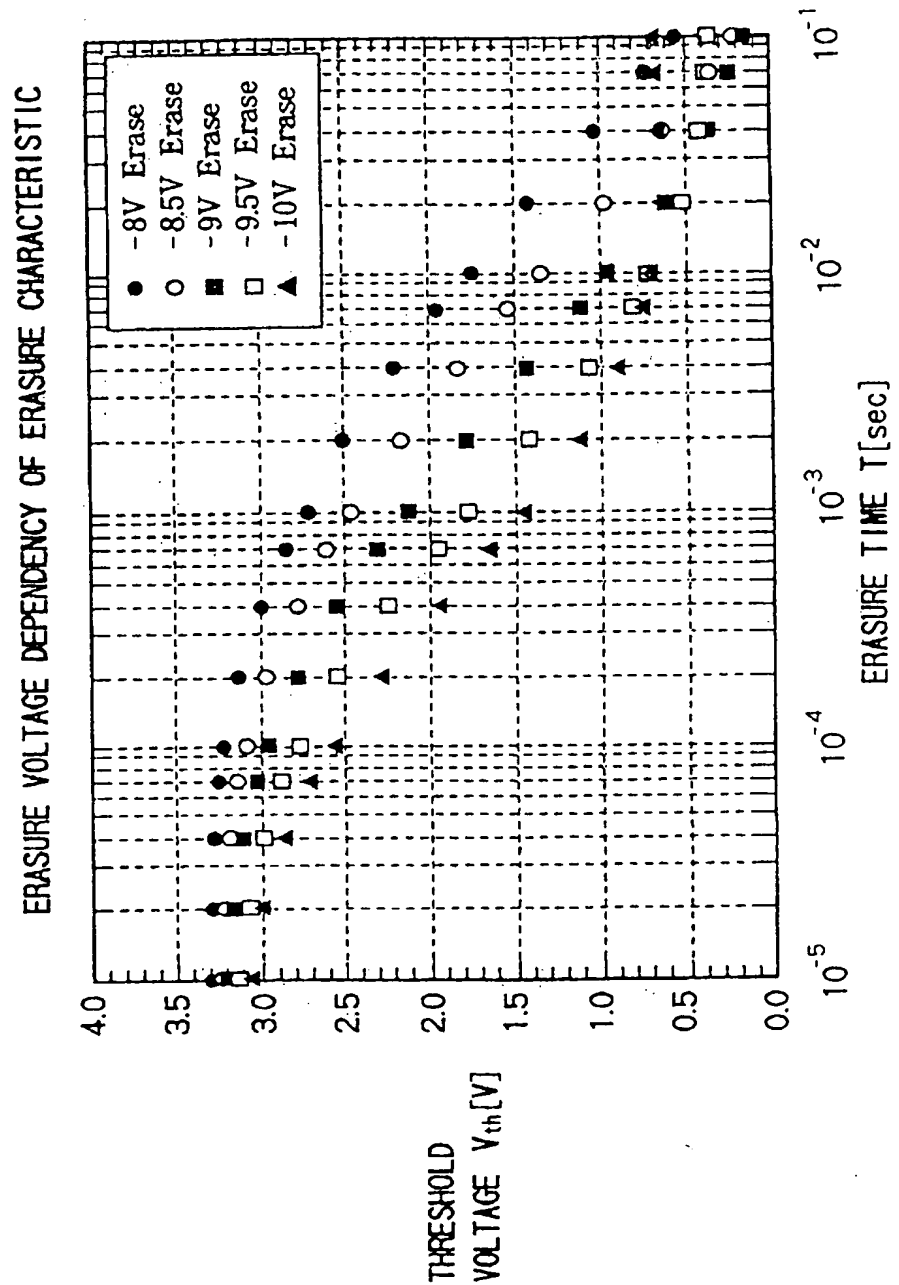


FIG.8



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FIG.9

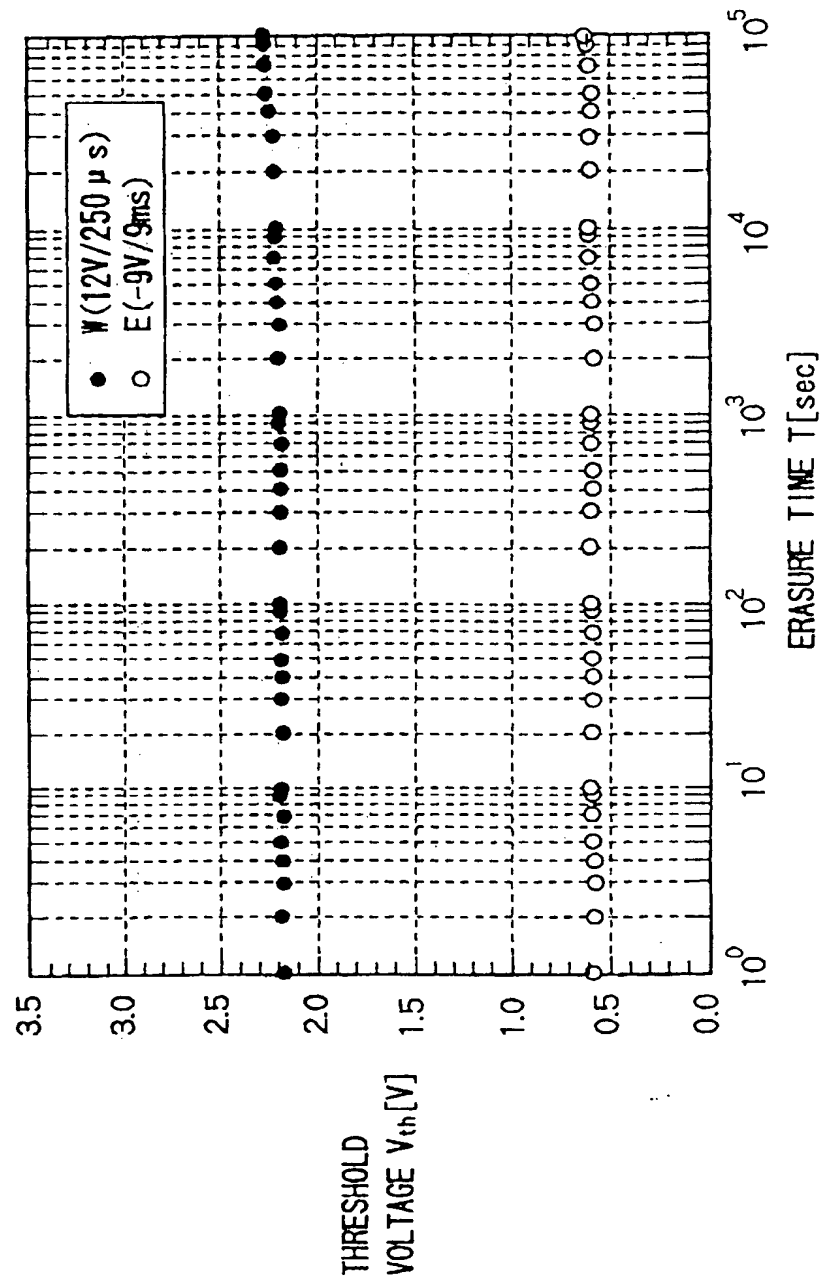
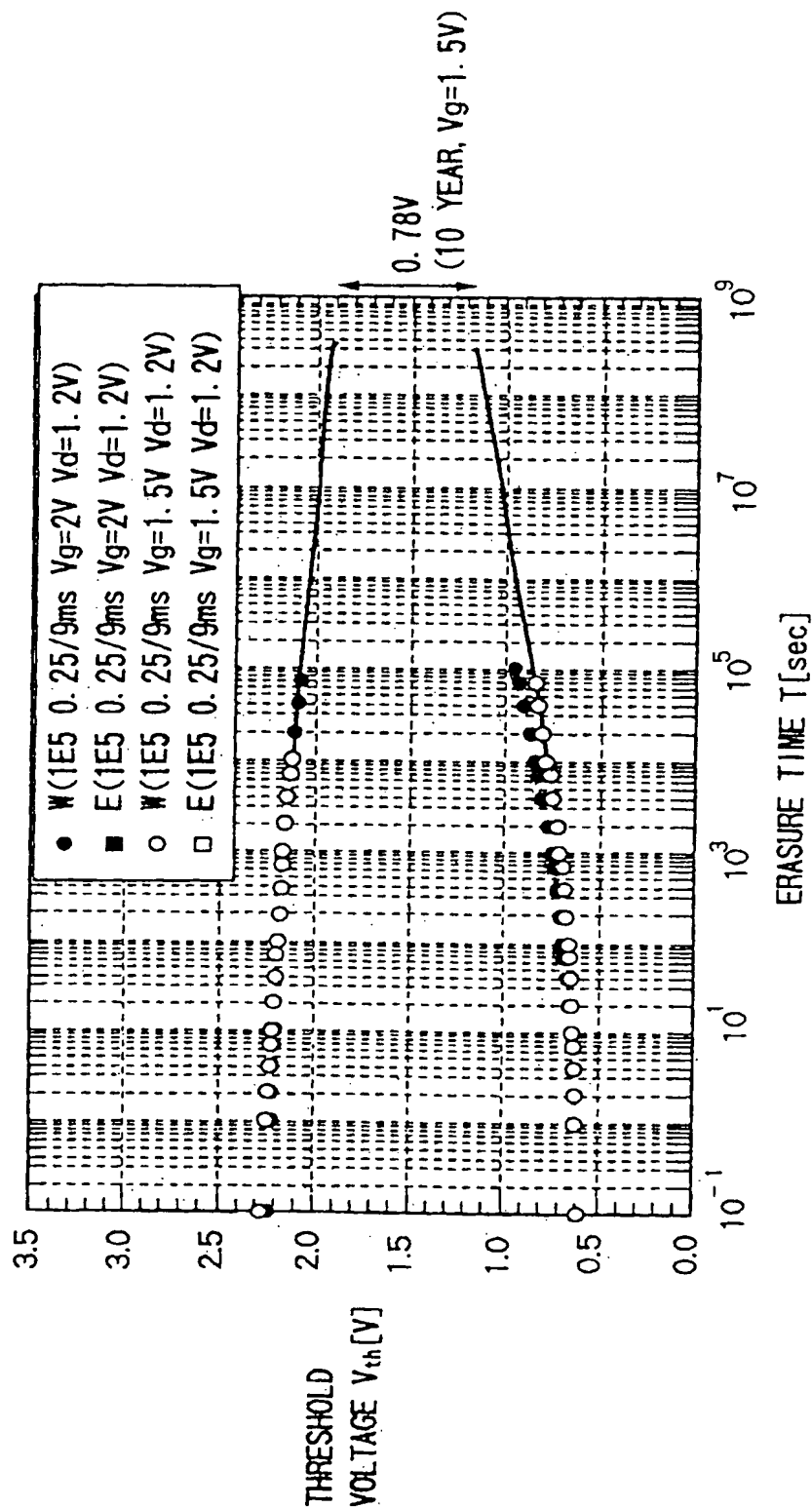
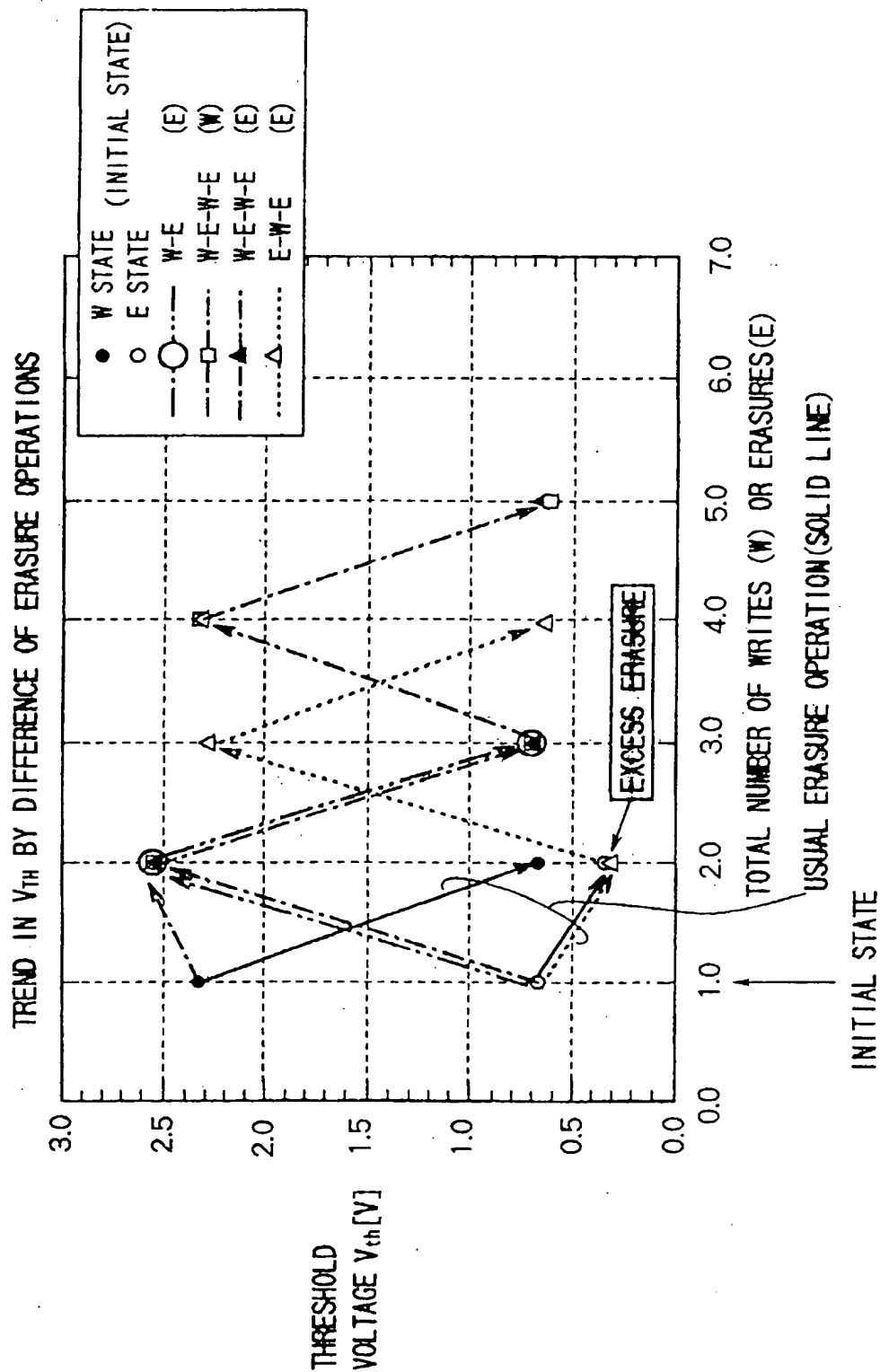


FIG.10



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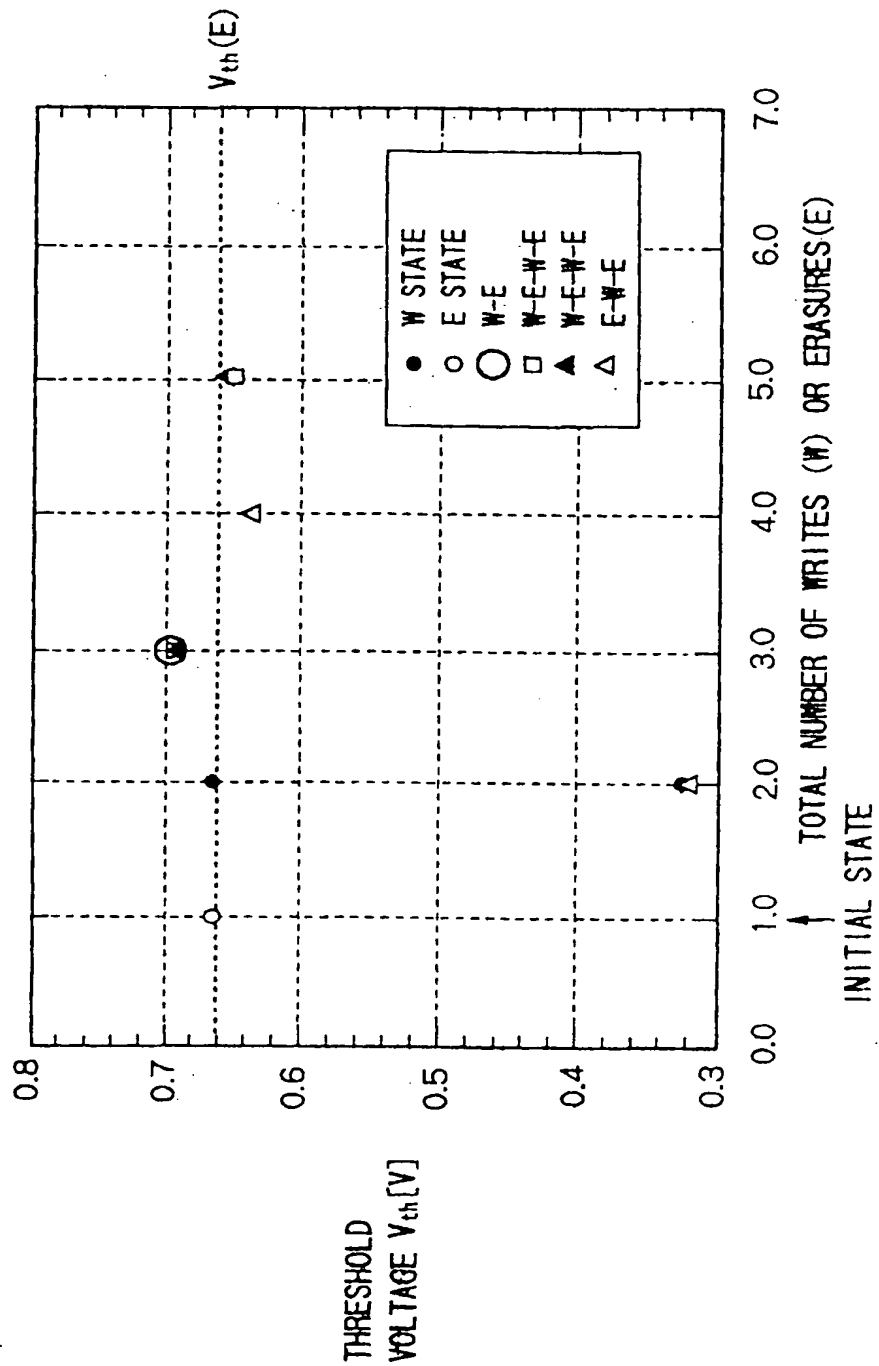
FIG.11



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FIG.12

ENLARGED VIEW OF V_{th} TREND



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ERASURE OPERATION ACCORDING TO PRESENT INVENTION

FIG.13A

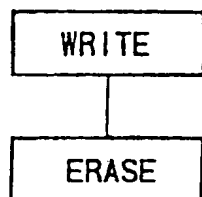


FIG.13B

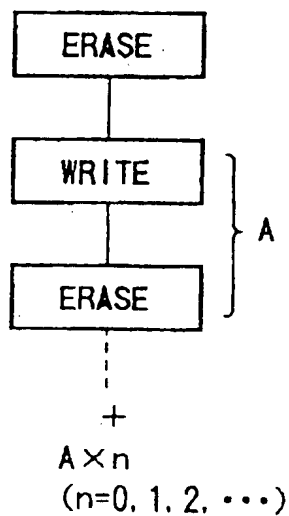
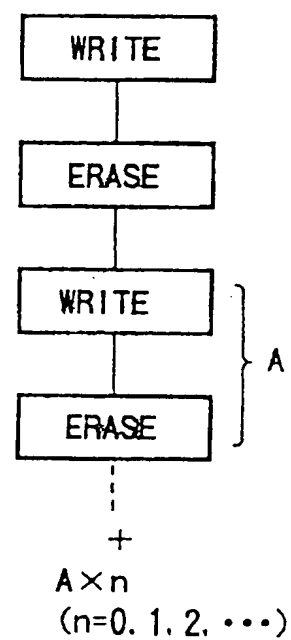


FIG.13C



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FIG.14

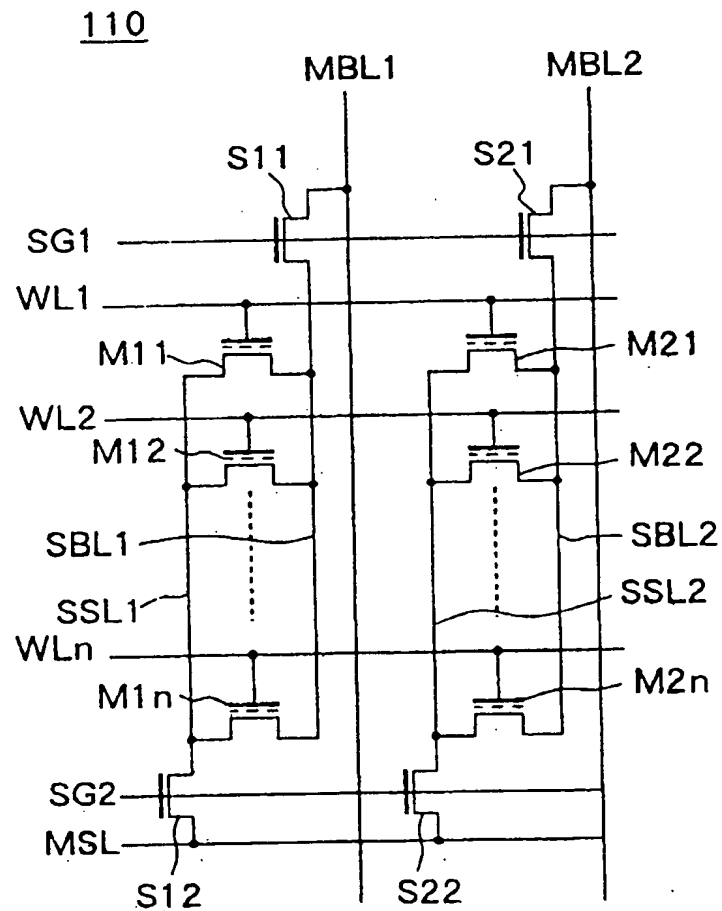


FIG. 15

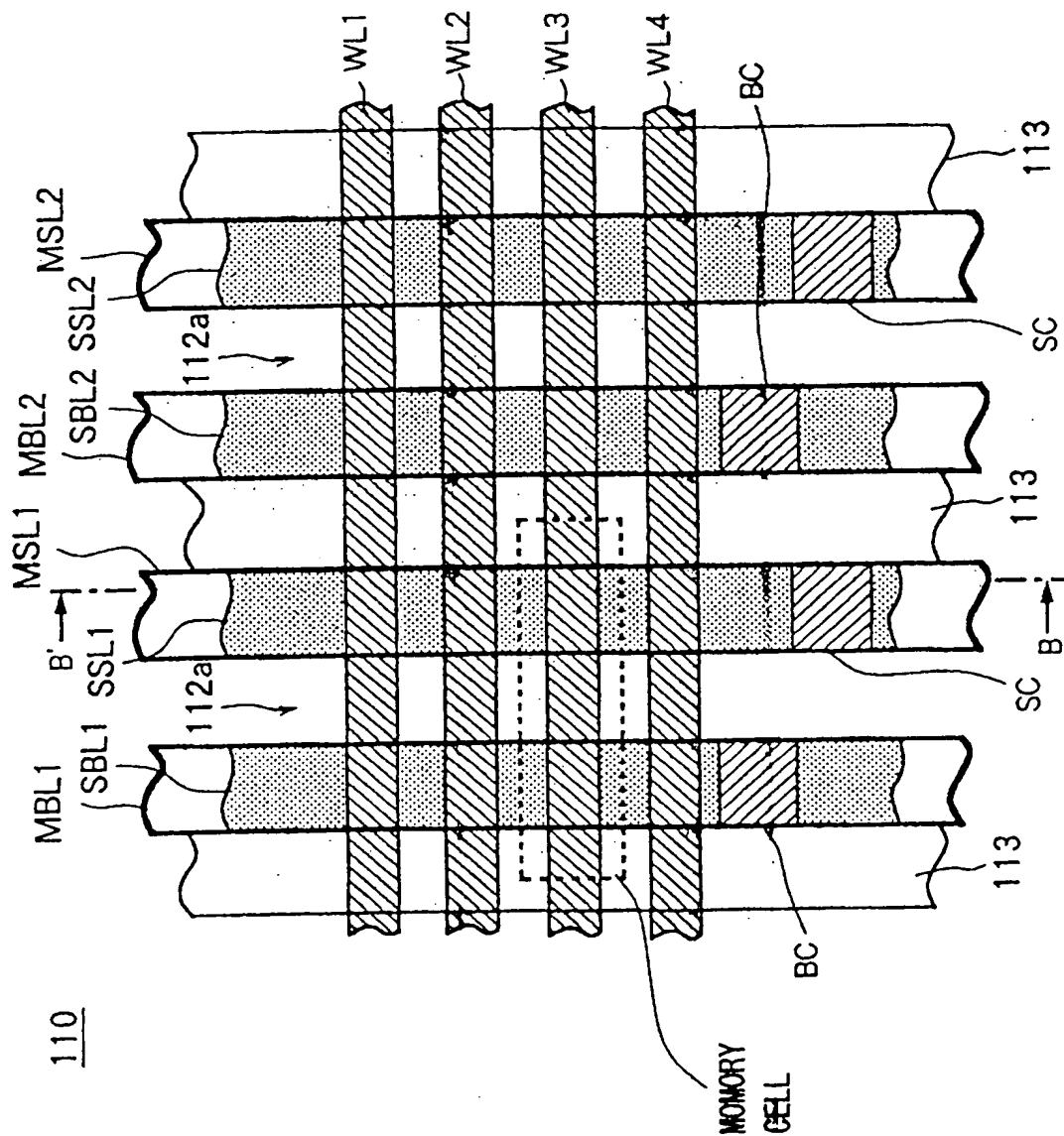


FIG.16

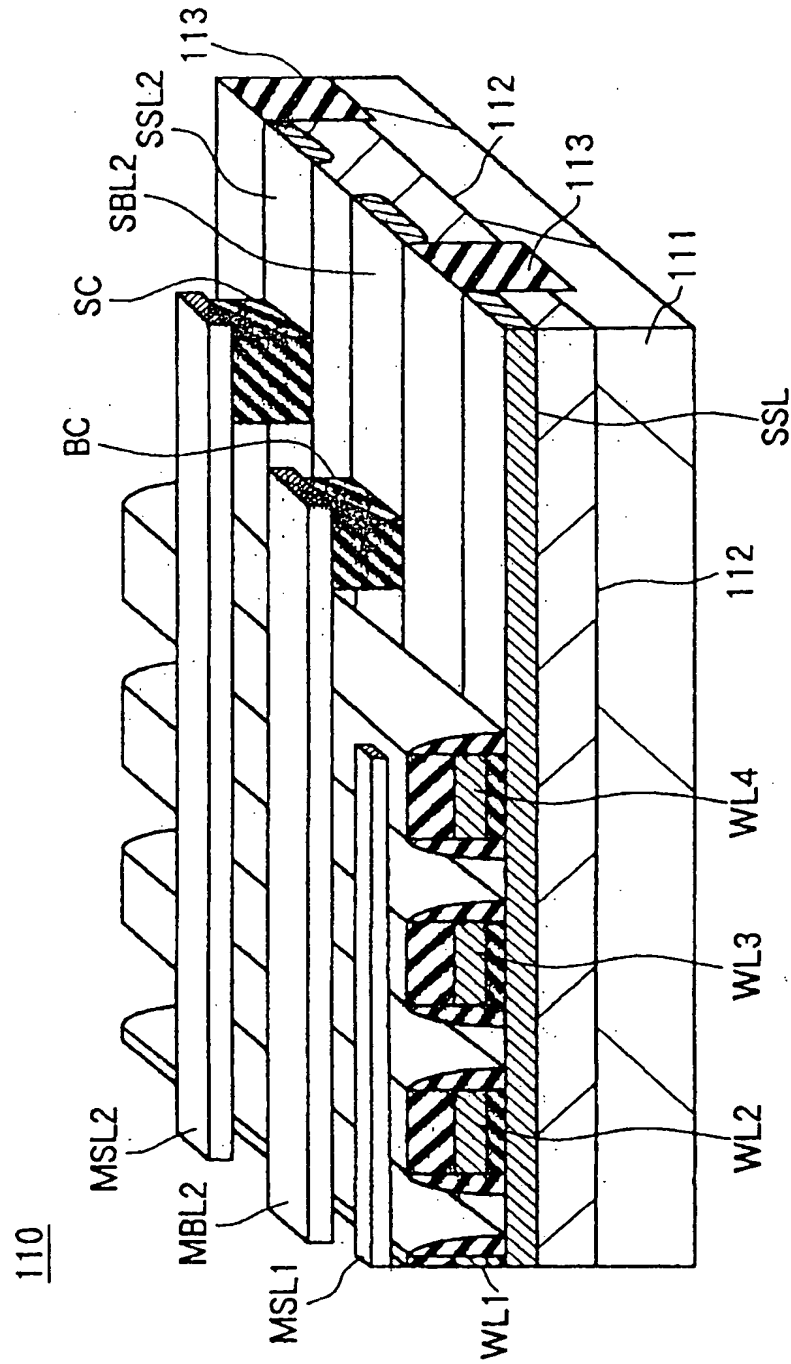


FIG.17

